## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.

10/510,567

Confirmation No.: 9020

First Named Inventor

: Hirohisa MIYAZAWA : October 8, 2004

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Examiner

: TUAN T. DINH

Docket No.

029267.55488US

Customer No.

23911

Title

: Circuit Board Device for Information Apparatus, Multilayered Module Board and Navigation System

## REPLY AFTER FINAL UNDER 37 C.F.R. §1.116

Mail Stop AF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated October 18, 2007. reconsideration and allowance of the above-identified application are respectfully requested. Claims 1, 3 and 5-10 remain pending.

Claims 1 and 5 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,477,593 to Khosrowpour et al. ("Khosrowpour"). This ground of rejection is respectfully traversed.

Prior to addressing this ground of rejection in detail, a brief summary of the disclosed invention is provided in order to highlight advantageous characteristics thereof.

Exemplary embodiments of the disclosed invention are directed to a circuit board structure that can be used in applications such as vehicle information